

**What Is Claimed Is:**

1           1.       An apparatus that implements a last-in first-out buffer, comprising:  
2           a plurality of cells arranged in a linear array to form the last-in first-out  
3       buffer;

4           wherein a given cell in the interior of the linear array is configured to  
5       receive get and put calls from a preceding cell in the linear array, and to make get  
6       and put calls to a subsequent cell in the linear array;

7           wherein if the given cell contains no data items, the given cell is  
8       configured to make a get call to retrieve a data item from the subsequent cell,  
9       whereby the data item becomes available in the given cell to immediately satisfy a  
10      subsequent get call to the given cell from the preceding cell without having to  
11      wait for the data item to propagate to the given cell from subsequent cells in the  
12      linear array; and

13          wherein if the given cell contains no space for additional data items, the  
14      given cell is configured to make a put call to transfer a data item to the subsequent  
15      cell, whereby space becomes available in the given cell to immediately satisfy a  
16      subsequent put call to the given cell from the preceding cell without having to  
17      wait for data in the given cell to propagate to subsequent cells in the linear array.

1           2.       The apparatus of claim 1, wherein communications between the  
2       plurality of cells take place asynchronously without reference to a system clock  
3       signal.

1           3.       The apparatus of claim 1, wherein the given cell includes:  
2       a master location for storing a data item; and

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3 a slave location for temporarily storing a new data item during a put  
4 operation to the given cell until a preexisting data item in the master location can  
5 be moved to the subsequent cell to make room for the new data item.

1 4. The apparatus of claim 1, wherein the given cell includes a first  
2 location and a second location for storing data items.

1 5. The apparatus of claim 1, wherein the given cell includes a first  
2 location, a second location and a third location for storing data items.

1 6. The apparatus of claim 1, wherein the given cell includes more  
2 than three locations for storing data items.

1 7. The apparatus of claim 1, wherein each cell in the linear array  
2 includes circuitry to determine if all subsequent cells in the linear array are  
3 completely full.

1 8. The apparatus of claim 1, wherein each cell in the linear array  
2 includes circuitry to determine if all subsequent cells in the linear array are empty.

1 9. An apparatus that implements a last-in first-out buffer, comprising:  
2 a plurality of cells arranged in a linear array to form the last-in first-out  
3 buffer;

4 wherein a given cell in the interior of the linear array is configured to  
5 receive get and put calls from a preceding cell in the linear array, and to make get  
6 and put calls to a subsequent cell in the linear array;

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7 wherein if the given cell contains no data items, the given cell is  
8 configured to make a get call to retrieve a data item from the subsequent cell,  
9 whereby the data item becomes available in the given cell to immediately satisfy a  
10 subsequent get call to the given cell from the preceding cell without having to  
11 wait for the data item to propagate to the given cell from subsequent cells in the  
12 linear array;

13 wherein if the given cell contains no space for additional data items, the  
14 given cell is configured to make a put call to transfer a data item to the subsequent  
15 cell, whereby space becomes available in the given cell to immediately satisfy a  
16 subsequent put call to the given cell from the preceding cell without having to  
17 wait for data in the given cell to propagate to subsequent cells in the linear array;

18 wherein communications between the plurality of cells take place  
19 asynchronously without reference to a system clock signal;

20 wherein each cell in the linear array includes circuitry to determine if all  
21 subsequent cells in the linear array are completely full; and

22 wherein each cell in the linear array includes circuitry to determine if all  
23 subsequent cells in the linear array are empty.

1 10. The apparatus of claim 9, wherein the given cell includes:  
2 a master location for storing a data item; and  
3 a slave location for temporarily storing a new data item during a put  
4 operation to the given cell until a preexisting data item in the master location can  
5 be moved to the subsequent cell to make room for the new data item.

1 11. The apparatus of claim 9, wherein the given cell includes a first  
2 location and a second location for storing data items.



